

### Features

- THE REVERSE CONSTRUCTION PROVIDES LOWER ESR & ESL.
- HI SELF RESONANT FREQUENCY MAKES THE GOOD NOISE SUPPRESSION.
- SUITABLE FOR DECOUPLING APPLICATIONS.
- DUE TO THE LOW PROFILE CONSTRUCTION ,IT IS SUITABLE FOR THE SET PRODUCTS WITH HEIGHT RESTRICTION.

### ESL COMPARISON

CHIP SIZE		ESL	pH
C1632(0612)	Flip	25%	250~300
C3216(1206)	STD	100%	1,000~1,300
C1220(0508)	Flip	35%	350~400
C2012(0805)	STD	70%	700~800
C0816(0306)	Flip	30%	300~350
C1608(0603)	STD	50%	500~700

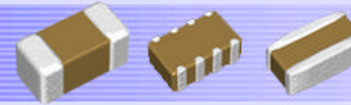
\* ESL(%) is relative figure compared with C3216(1206)

ESR differs by capacity respectively.

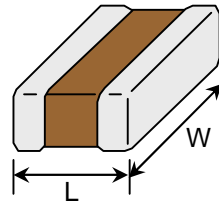
ESR value of a flip chip cap is about 20 ~ 30 % of the multilayer ceramic chip capacitor with the same capacitance and case size.

### Applications

- BIAS LINE IN CPU.
- BIAS LINE IN HIGH SPEED DIGITED IC.



### Dimensions



(Unit : mm)

	C0816(0306)	C1220(0508)	C1632(0612)
<b>L</b>	0.80 ± 0.10	1.25 ± 0.20	1.60 ± 0.20
<b>W</b>	1.60 ± 0.10	2.00 ± 0.20	3.20 ± 0.20

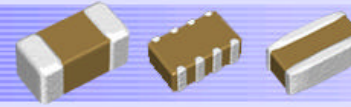
### Cap values & Thickness

Dec.2004

Type		0306(C0816)					0508(C1220)					0612(C1632)				
Temp.chara		X7R			X5R		X7R			X5R		X7R			X5R	
Cap. 3digit	WV (μF)	1H	1E	1C	1A	0J	1H	1E	1C	1A	0J	1H	1E	1C	1A	0J
		50V	25V	16V	10V	6.3V	50V	25V	16V	10V	6.3V	50V	25V	16V	10V	6.3V
103	0.01															
223	0.022															
473	0.047															
104	0.1															
224	0.22															
474	0.47															
105	1															
225	2.2															
475	4.7															

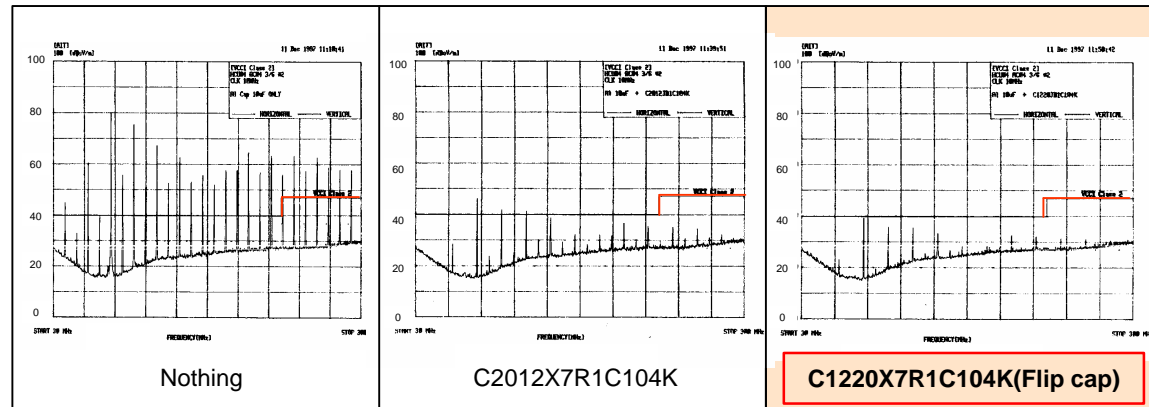
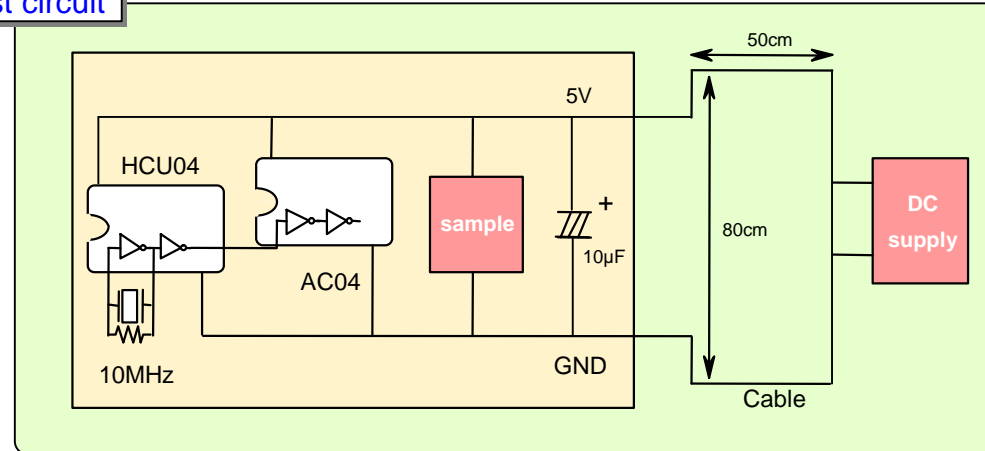
■ 0.5 ± 0.1   
 ■ 0.7 ± 0.1   
 ■ 0.85 ± 0.15   
 ■ 1.15 ± 0.15   
 ■ 1.30 ± 0.15

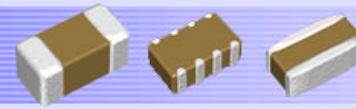
**Please contact us for the other cap value availability**



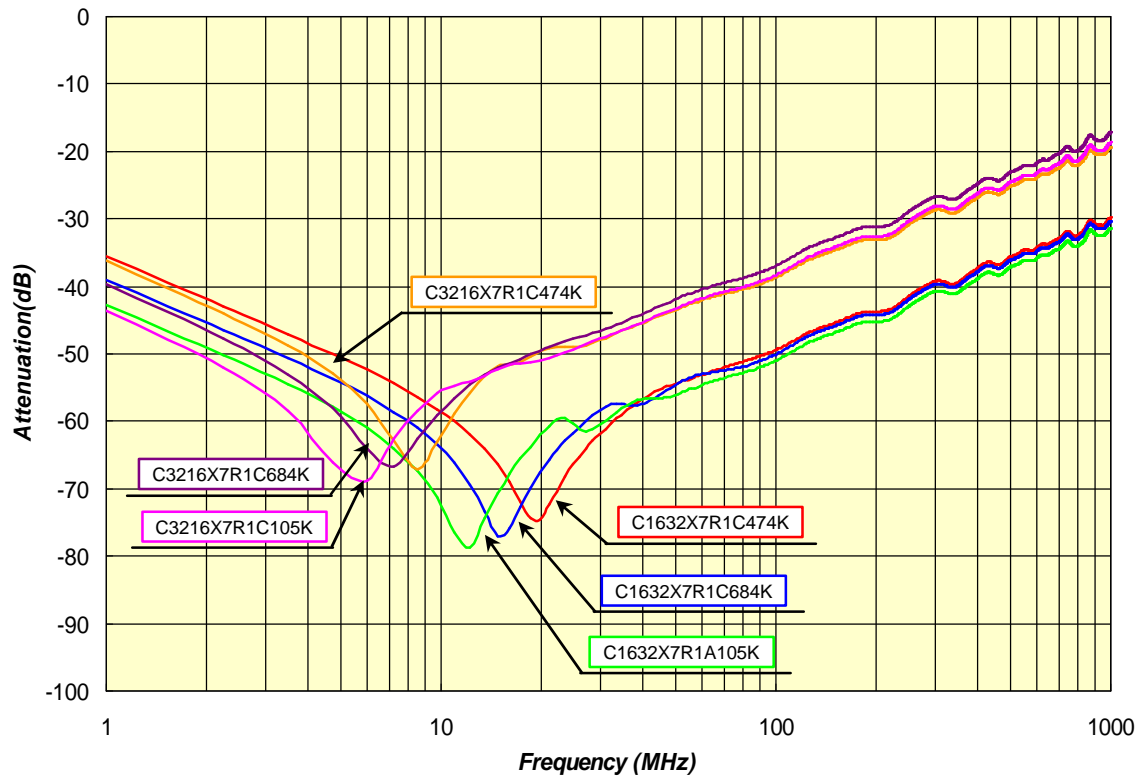
How to evaluate noise suppression effect of Decoupling capacitor

Test circuit

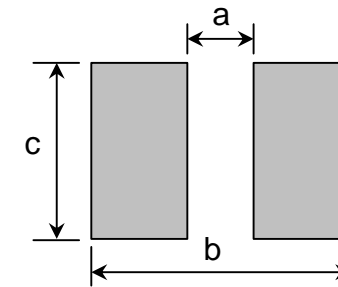




### Attenuation



### Recommended land pattern



(unit:mm)

	C0816(0306)	C1220(0508)	C1632(0612)
a	0.3	0.5	0.75
b	1.0	1.6	2.2
c	1.6	2.0	3.2

Contents might be changed without notice